

The SynchAADL2Maude Tool Demo

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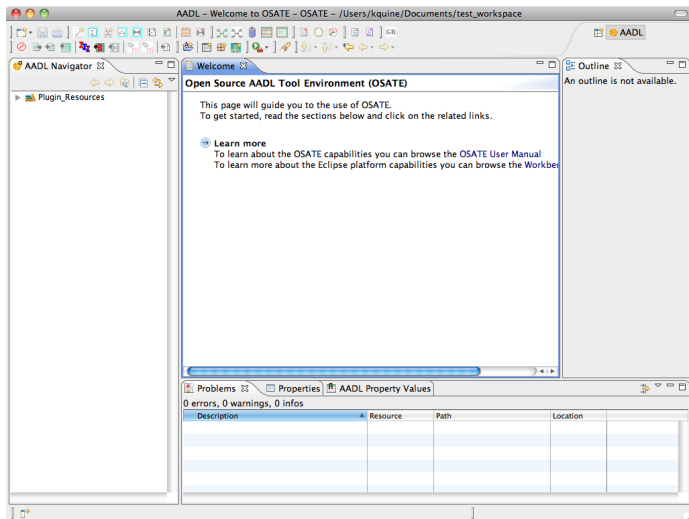
²University of Oslo

Outline

- 1 Basic OSATE
- 2 Invoking SynchAADL2Maude
- 3 Synchronous AADL Constraints Checker
- 4 Code Generation and Simulation
- 5 Model Checking Synchronous AADL Models

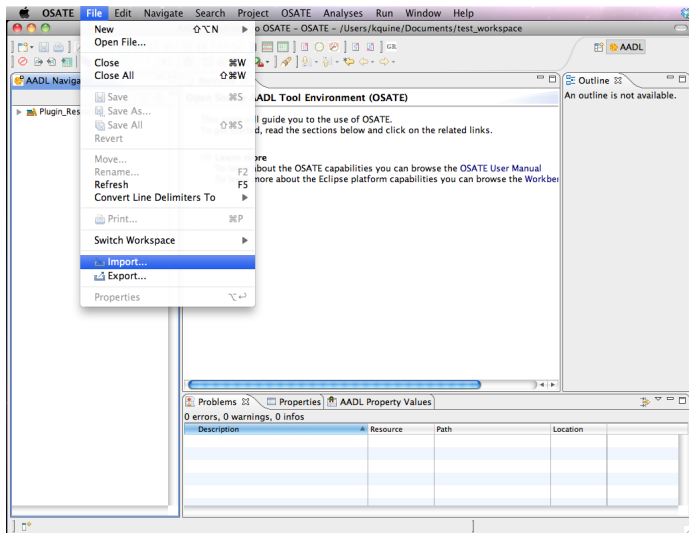
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- **OSATE** is a toolset for AADL given by a set of Eclipse plugins.
- This is the first screen that you can see when you execute OSATE.



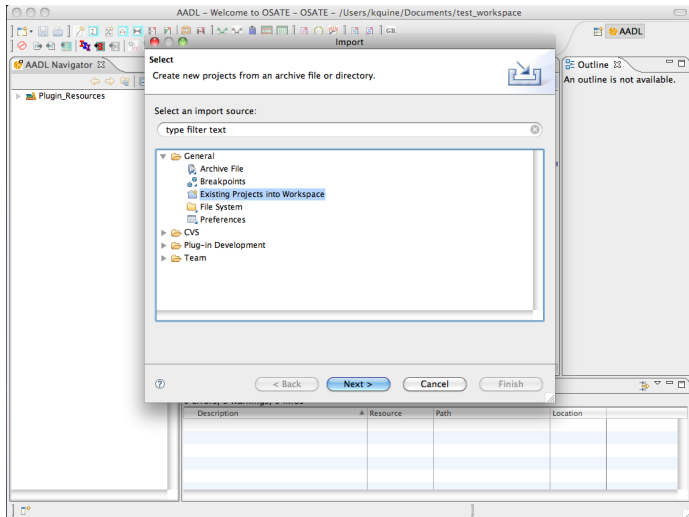
OSATE - Importing an Example (I)

- We start with a simple example.
- First, we will import the Active Standby example.



OSATE - Importing an Example (II)

- The active standby example in our tool webpage can be imported as an existing project.



The Active Standby Example - Text

- Main.aadl is a top-level system file that shows a brief architecture.
- **SynchAADL** properties are declared here, to express that this system is in **Synchronous AADL**

The screenshot shows the AADL IDE interface. The main editor displays the content of Main.aadl, which defines a package MainModule containing a system ActiveStandbySystem and its implementation ActiveStandbySystem.impl. The implementation includes subcomponents, connections, and properties. A red arrow points to the line defining the SynchAADL::syncPeriod property.

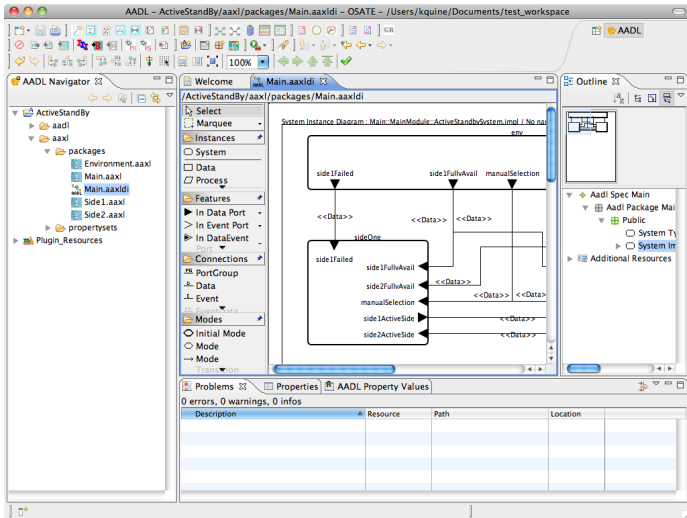
```
package MainModule
public
  system ActiveStandbySystem
end ActiveStandbySystem;

system implementation ActiveStandbySystem.impl
subcomponents
  sideOne: system Side1::Side1.impl;
  sideTwo: system Side2::Side2.impl;
  env: system Environment::Environment.impl;
connections
  C1: data part sideOne.side1ActiveSide ->> sideTwo.side1ActiveSide;
  C2: data part sideTwo.side2ActiveSide ->> sideOne.side2ActiveSide;
  F1: data part env.side1FullyAvail ->> sideOne.side1FullyAvail;
  F2: data part env.side1FullyAvail ->> sideTwo.side1FullyAvail;
  F3: data part env.side2FullyAvail ->> sideOne.side2FullyAvail;
  F4: data part env.side2FullyAvail ->> sideTwo.side2FullyAvail;
  C3: data part env.manualSelection ->> sideOne.manualSelection;
  C4: data part env.manualSelection ->> sideTwo.manualSelection;
  C5: data part env.side1Failed ->> sideOne.side1Failed;
  C6: data part env.side2Failed ->> sideTwo.side2Failed;
properties
  SynchAADL::Synchronous => true;
  SynchAADL::syncPeriod => 2 Ms;
  Period => 2 Ms;
end ActiveStandbySystem.impl;
end MainModule;
```

The AADL Navigator on the left shows the project structure, and the Outline on the right shows the package MainModule. The Problems, Properties, and AADL Property Values panels are visible at the bottom.

The Active Standby Example - Graphic

- The AADL graphical model of the active standby example is also given in the file Main.aaxldi



The Active Standby Example - XML

- The AADL XML model of the active standby example is automatically generated by OSATE in the file `Main.aaxl`

The screenshot displays the AADL IDE interface with the following components:

- Window Title:** AADL - ActiveStandBy/aaxl/packages/Main.aaxl - OSATE - /Users/kquine/Documents/test_workspace
- Toolbar:** Standard IDE navigation and editing tools.
- AADL Navigator:** Shows a tree view of the project structure:
 - ActiveStandBy
 - aadl
 - packages
 - propertysets
 - aaxl
 - packages
 - Environment.aaxl
 - Main.aaxl (selected)
 - Main.aaxl.di
 - Side1.aaxl
 - Side2.aaxl
 - propertysets
 - Plugin_Resources
- Editor:** Displays the XML content for `platform:/resource/ActiveStandBy/aaxl/packages/Main.aaxl`. The visible XML structure is:

```
<Aadl Spec Main>  
  <Aadl Package MainModule>  
    <Public>  
      <System Type MainModule::ActiveStandbySystem>  
      <System Impl MainModule::ActiveStandbySystem.impl>  
    </Public>  
  </Aadl Package MainModule>  
</Aadl Spec Main>
```
- Outline:** Shows the project's outline structure:
 - Aadl Spec Main
 - Aadl Package Main
 - Public
 - System Ty
 - System Im
- Problems/Properties/AADL Property Values:** Shows 0 errors, 0 warnings, and 0 infos.
- Table:** A table with columns: Description, Resource, Path, Location.
- Status Bar:** Selected Object: System Impl MainModule::ActiveStandbySystem.impl

The Active Standby Example - Instance Model

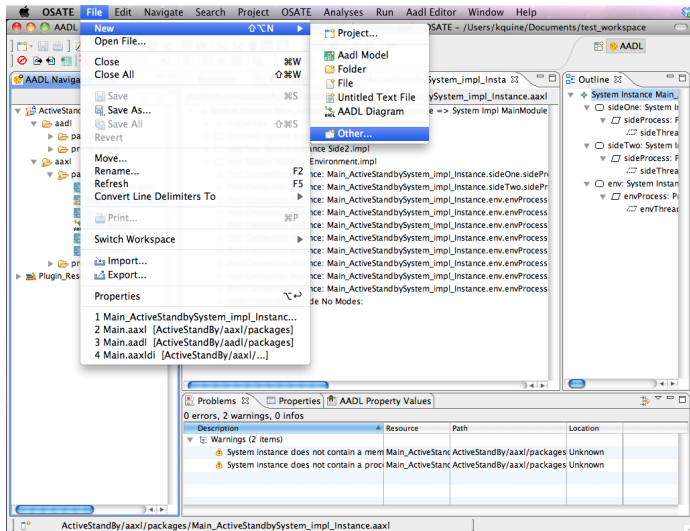
- We can create an instance model from a system implementation by pressing the **Instantiate system** button.
- The top level system implementation of the active standby system is instantiated here.

The screenshot displays the AADL IDE interface. The **AADL Navigator** on the left shows a project structure with a system implementation `Main_ActiveStandbySystem_impl_Instance.aaxl` selected. A red arrow points to the **Instantiate system** button in the toolbar. The main editor shows the instance model tree for `System Instance Main_ActiveStandbySystem_impl_Instance => System Impl MainModule`, listing various components like `sideOne`, `sideTwo`, and `env`. The **Problems** window at the bottom shows two warnings: "System instance does not contain a mem Main_ActiveStandbySystem_impl_Instance.env.envProcess" and "System instance does not contain a proc Main_ActiveStandbySystem_impl_Instance.env.envProcess".

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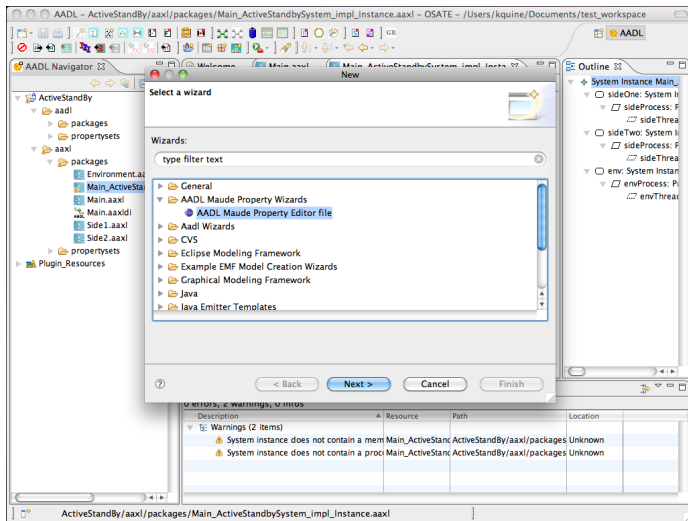
Invoking the SynchAADL2Maude Window

- The **SynchAADL2-Maude** window can be invoked from an AADL instant model.



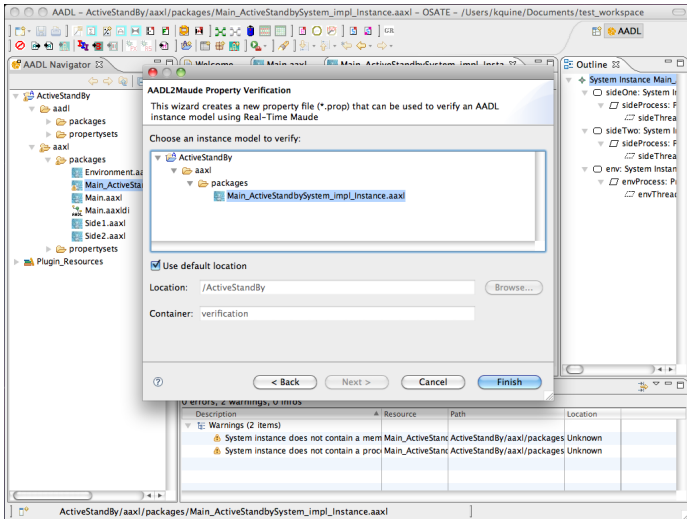
AADL Maude Property Editor Wizards

- From the File menu, we can create an AADL Maude Property Editor file.



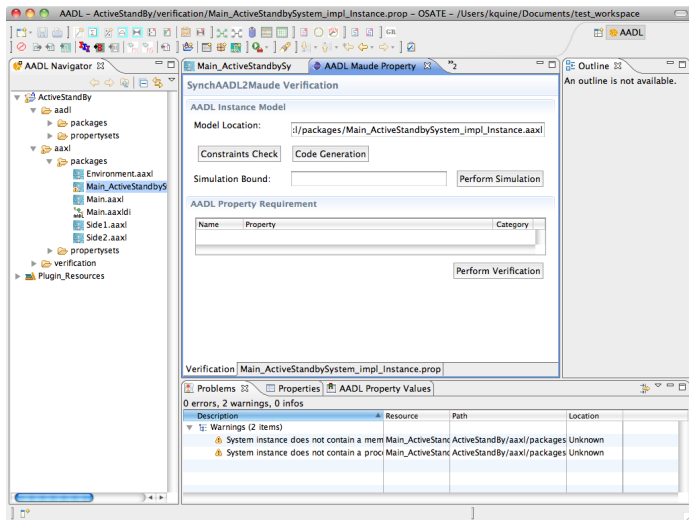
Creating an AADL Maude Property File

- We can choose any valid AADL instance model from the wizard.



The SynchAADL2Maude Window

- This screen shows the SynchAADL2-Maude window.
- There are four buttons in this window:
Constraints Check,
Code Generation,
Perform Simulation, and
Perform Verification.

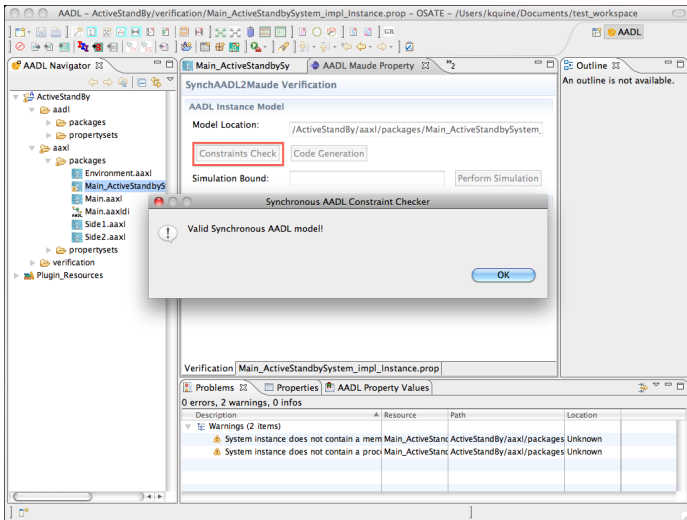


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Checking SynchAADL Constraints

- We can check SynchAADL constraints by clicking on the **Constraints Check** button.



SynchAADL Constraints - Erroneous Cases (I)

- What if some SynchAADL constraint is not satisfied?
- We add an invalid immediate connection, and see what happened.

The screenshot shows the AADL IDE interface. The main editor displays the code for `MainModule` and `ActiveStandbySystem`. A red arrow points to the connection `C1: data port sideOne.side1ActiveSide -> sideTwo.side1ActiveSide;` in the `connections` section. The `properties` section shows `SynchAADL::Synchronous -> true;` and `SynchAADL::syncPeriod -> 2 Ms;`. The `Problems` window at the bottom shows two warnings:

Description	Resource	Path	Location
System instance does not contain a mem	Main_ActiveStand	ActiveStandBy/aaxl/packages	Unknown
System instance does not contain a proc	Main_ActiveStand	ActiveStandBy/aaxl/packages	Unknown

SynchAADL Constraints - Erroneous Cases (II)

- Our tool then notifies errors.

The screenshot displays the AADL IDE interface. The main window shows the 'SynchAADL2Maude Verification' results, including the 'AADL Instance Model' and 'Model Location'. A modal dialog box is open, displaying an error message: '(Side1Process.impl)sideThread.side1ActiveSide-> (Side2Process.impl)sideThread.side1ActiveSide: Invalid immediate communication'. The 'Problems' view at the bottom shows two warnings:

Description	Resource	Path	Location
System instance does not contain a mem	Main_ActiveStand	ActiveStandBy/aaxl/packages	Unknown
System instance does not contain a proc	Main_ActiveStand	ActiveStandBy/aaxl/packages	Unknown

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The Active Standby Example

- Let us go back to the correct model.

The screenshot displays the AADL IDE interface. The main editor shows the code for `MainModule` in `Main.aadl`. The code defines a public system `ActiveStandbySystem` and its implementation `ActiveStandbySystem.impl`. The implementation includes subcomponents `sideOne` and `sideTwo`, an environment `env`, and a set of connections (C1-C6) linking data ports between the sides and the environment. Properties for synchronization and timing are also defined.

```
package MainModule
public
  system ActiveStandbySystem
end ActiveStandbySystem;

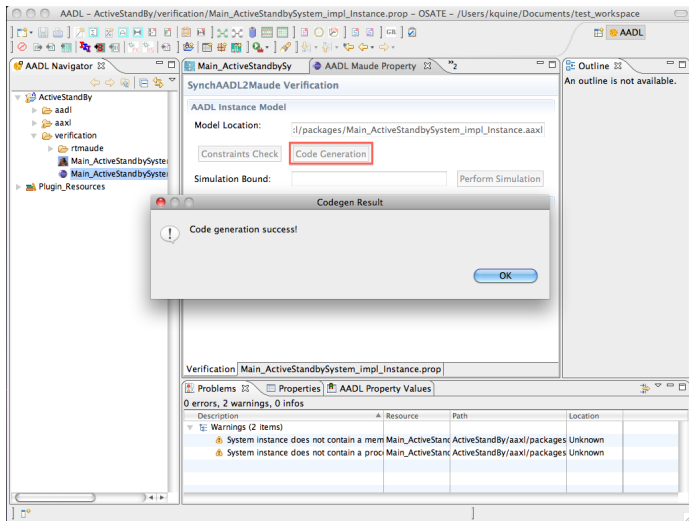
system implementation ActiveStandbySystem.impl
  subcomponents
    sideOne: system Side1::Side1.impl;
    sideTwo: system Side2::Side2.impl;
    env: system Environment::Environment.impl;
  connections
    C1: data port sideOne.side1ActiveSide ->> sideTwo.side1ActiveSide;
    C2: data port sideTwo.side2ActiveSide ->> sideOne.side2ActiveSide;
    F1: data port env.side1FullyAvail -> sideOne.side1FullyAvail;
    F2: data port env.side1FullyAvail -> sideTwo.side1FullyAvail;
    F3: data port env.side2FullyAvail -> sideOne.side2FullyAvail;
    F4: data port env.side2FullyAvail -> sideTwo.side2FullyAvail;
    C3: data port env.manualSelection -> sideOne.manualSelection;
    C4: data port env.manualSelection -> sideTwo.manualSelection;
    C5: data port env.side1Failed -> sideOne.side1Failed;
    C6: data port env.side2Failed -> sideTwo.side2Failed;
  properties
    SynchAADL::Synchronous => true;
    SynchAADL::syncPeriod => 2 Ms;
    Period => 2 Ms;
  end ActiveStandbySystem.impl;
end MainModule;
```

The Problems window at the bottom shows two warnings:

Description	Resource	Path	Location
System instance does not contain a mem Main_ActiveStandby/aaxl/packages	Unknown		
System instance does not contain a proc Main_ActiveStandby/aaxl/packages	Unknown		

Real-Time Maude Code Generation (I)

- We can automatically create the corresponding Real-Time Maude model from a Synchronous AADL model by clicking on the **Code Generation** button.



Real-Time Maude Code Generation (II)

- We can find the generated Real-Time Maude model on the AADL navigator sidebar.

The screenshot displays the AADL IDE interface. On the left, the AADL Navigator sidebar shows a tree structure with 'Main_ActiveStandbySystem' selected, indicated by a red arrow. The main editor window shows the generated Real-Time Maude code, which includes definitions for names, states, variables, and the initial state. The code is as follows:

```
load rtmaude/synch-aadl-interpreter.maude
(comod MainActiveStandbySystemimplInstance is
  including SYNCHRONOUS-STEP .

--- names, states, variables
ops s2F s1FA mS s1F prevmanualSelection s2FA : -> BoolVarId [ctor] .
ops prevSide2ActiveSide prevSide1ActiveSide : -> IntVarId [ctor] .
ops s0 side1WaitState side2ActiveState side1FailedState side2FailedState
ops impl : -> ImplName [ctor] .
ops Side1::Side1Thread MainModule::ActiveStandbySystem Side2::Side2 Env
ops side1ActiveSide side1Failed manualSelection side2ActiveSide side2Failed
ops MainActiveStandbySystemimplInstance sideThread envProcess envThread

--- the initial state
op initial : -> Configuration [memo] .
eq initial = transform(MainActiveStandbySystemimplInstance : system Main
eq MAIN = MainActiveStandbySystemimplInstance .

-----
----- AADL instance
-----

var COMP : ComponentId .

eq stateVariables(thread Side1::Side1Thread . impl) =
  (prevSide2ActiveSide l-> 0)
  (prevmanualSelection l-> false) .

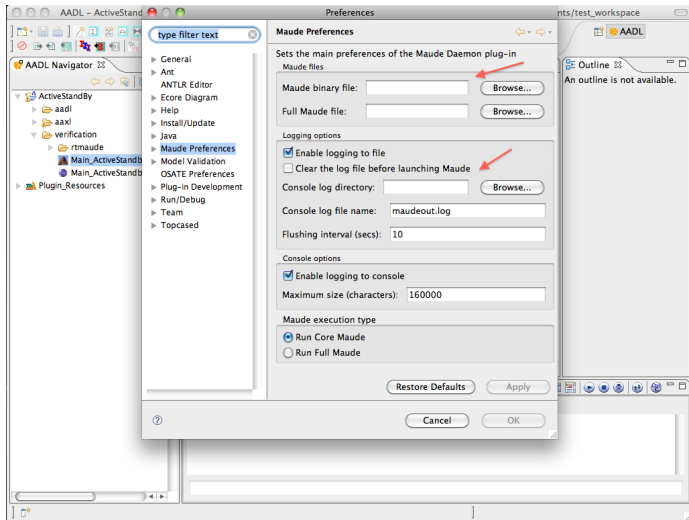
eq states(thread Side1::Side1Thread . impl) =
```

At the bottom of the IDE, the Problems window shows two warnings:

Description	Resource	Path	Location
System instance does not contain a mem Main_ActiveStandbySystem/aaxl/packages Unknown			
System instance does not contain a proc Main_ActiveStandbySystem/aaxl/packages Unknown			

Maude Development Tool Setting

- When a Maude file is first executed, the MDT setting window is popped-up.
- The correct paths of both a Maude binary file and a Full Maude file should be inserted.
- If “logging to file” is enabled, we should also insert a console log directory.



SynchAADL Simulation in Real-Time Maude

- We can simulate a given model within some bound by pressing the **Perform Simulation** button.
- The result will be shown in the Maude Console.

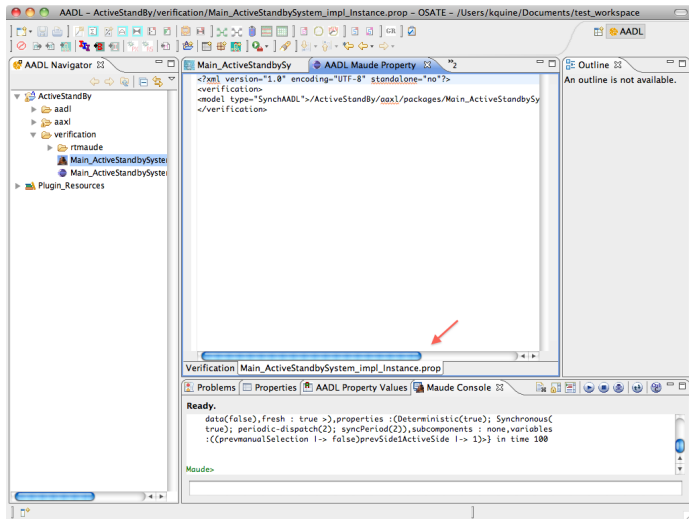
The screenshot displays the AADL IDE interface for a simulation. The main window is titled "SynchAADL2Maude Verification" and shows the "AADL Instance Model" configuration. The "Model Location" is set to `!:/packages/Main_ActiveStandbySystem_impl_Instance.aaxl`. The "Simulation Bound" is set to 100. The "Perform Simulation" button is highlighted with a red box. Below the model configuration, there is a table for "AADL Property Requirement" with columns for Name, Property, and Category. The "Perform Verification" button is also visible. At the bottom, the "Maude Console" shows the simulation result: "Ready. data(false),fresh : true >),properties :(Deterministic(true); Synchronous(true); periodic-dispatch(2); syncPeriod(2); subcomponents : none,variables :((premanualSelection l-> false)prevSideActiveSide l-> 1)> in time 100". A red arrow points to the word "Result" next to the output.

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XML Property File (I)

- AADL Maude property files are actually XML files.
- We can see and modify the content of the file by clicking on the right tab at the bottom.



Model Checking LTL Specifications (I)

- The LTL specification to be verified are shown in the AADL Property Requirement table.

The screenshot displays the AADL IDE interface for the verification of the Main_ActiveStandbySystem. The AADL Navigator on the left shows the project structure, with the 'Main_ActiveStandbySystem' property requirement table selected. The main workspace shows the 'AADL Maude Property' configuration, including the model location and simulation options. The 'AADL Property Requirement' table lists several LTL properties (R1, R2a, R3g, R4, R5side1) with their corresponding logical expressions and categories. The 'Maude Console' at the bottom shows the 'Ready.' status and the initial state of the system.

AADL Property Requirement Table:

Name	Property	Category
R1	$O \{ () \} (\text{noChangeAssumptionNextState} \rightarrow O (\text{agreeOnActiveSide}))$	LTL
R2a	$O \{ () \} ((\text{noChangeAssumptionNextState} \wedge \text{side1FullyAvailable}) \wedge \text{side1FullyAv})$	LTL
R3g	$O \{ () \} (\sim \text{manSelectPressed} \wedge \text{agreeOnActiveSide} \wedge \text{side1FullyAv})$	LTL
R4	$O \{ () \} ((\text{side1Failed} \wedge \sim \text{side2Failed}) \rightarrow O (\sim \text{side2Failed} \rightarrow \text{side}))$	LTL
R5side1	$O \{ () \} ((\text{side1Active} \wedge \text{side1FullyAvailable} \wedge \sim \text{manSelectPressed})$	LTL

Maude Console Output:

```
Ready.  
data(false),fresh : true >),properties :{Deterministic(true); Synchronous(  
true); periodic-dispatch(2); syncPeriod(2); subcomponents : none, variables  
:((premanualSelection l-> false)prevSide1ActiveSide l-> 1)} in time 100  
  
Maude>
```

Model Checking LTL Specifications (II)

- When we press the **Perform Verification** button, the LTL properties in the table are model checked in Real-Time Maude.
- The model checking result will be shown in the Maude Console.

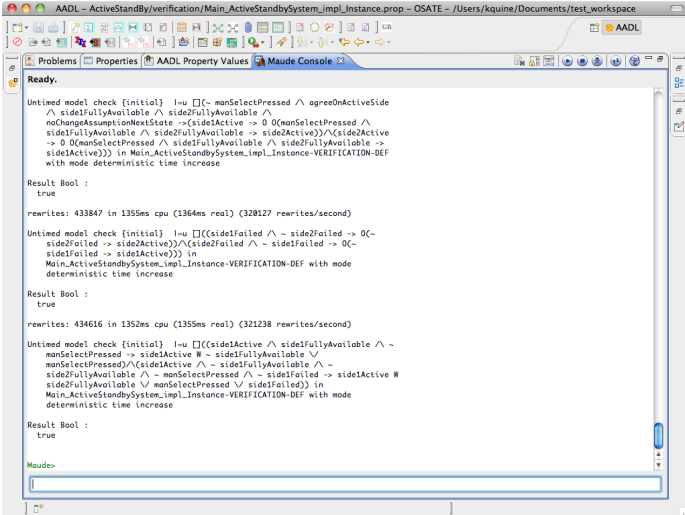
The screenshot displays the AADL IDE interface for model checking. The main window is titled "SynchAADL2Maude Verification" and shows the "AADL Instance Model" section with the "Model Location" set to `:/packages/Main_ActiveStandbySystem_impl_Instance.aaxl`. Below this, there are buttons for "Constraints Check", "Code Generation", and "Perform Simulation". The "AADL Property Requirement" section contains a table of LTL properties:

Name	Property	Category
R1	$O \{ () \} (\text{noChangeAssumptionNextState} \rightarrow O \text{ agreeOnActiveSide})$	LTL
R2a	$O \{ () \} ((\text{noChangeAssumptionNextState} \wedge / \text{ side1FullyAvailable})$	LTL
R3g	${} [] ((\sim \text{manSelectPressed}) \wedge \text{agreeOnActiveSide} \wedge / \text{ side1FullyAv})$	LTL
R4	${} [] ((\text{side1Failed} \wedge \sim \text{side2Failed}) \rightarrow O (\sim \text{side2Failed} \rightarrow \text{side}))$	LTL
R5side1	${} [] ((\text{side1Active} \wedge / \text{ side1FullyAvailable} \wedge \sim \text{manSelectPressed})$	LTL

A red box highlights the "Perform Verification" button. Below the table, the "Verification" section shows the file `Main_ActiveStandbySystem_impl_Instance.prop`. At the bottom, the "Maude Console" displays the result: "Elapsed time: 00:00:00.031" and "Result Bool : true". A red arrow points to the word "Result" in the console output.

Model Checking LTL Specifications (III)

- Here is the model checking result of the active standby example in a larger window.



The screenshot shows a Maude console window with the following content:

```
Ready.

Untimed model check {initial} l-u [](- manSelectPressed ^ agreeOnActiveSide
^ side1FullyAvailable ^ side2FullyAvailable ^
noChangeAssumptionNextState ->(side1Active -> 0 0(manSelectPressed ^
side1FullyAvailable ^ side2FullyAvailable -> side2Active))/^(side2Active
-> 0 0(manSelectPressed ^ side1FullyAvailable ^ side2FullyAvailable ->
side1Active))) in Main_ActiveStandbySystem_impl_Instance-VERIFICATION-DEF
with mode deterministic time increase

Result Bool :
true

rewrites: 433847 in 1355ms cpu (1364ms real) (320127 rewrites/second)

Untimed model check {initial} l-u []((side1Failed ^ ~ side2Failed -> 0(-
side2Failed -> side2Active))/^(side2Failed ^ ~ side1Failed -> 0(-
side1Failed -> side1Active))) in
Main_ActiveStandbySystem_impl_Instance-VERIFICATION-DEF with mode
deterministic time increase

Result Bool :
true

rewrites: 434616 in 1352ms cpu (1355ms real) (321238 rewrites/second)

Untimed model check {initial} l-u []((side1Active ^ side1FullyAvailable ^ ~
manSelectPressed -> side1Active # ~ side1FullyAvailable ^ ~
manSelectPressed)/^(side1Active ^ ~ side1FullyAvailable ^ ~
side2FullyAvailable ^ ~ manSelectPressed ^ ~ side1Failed -> side1Active #
side2FullyAvailable ^ manSelectPressed ^ side1Failed)) in
Main_ActiveStandbySystem_impl_Instance-VERIFICATION-DEF with mode
deterministic time increase

Result Bool :
true

Maude>
```

Model Checking LTL Specifications (IV)

- SynchAADL2- Maude creates the Real-Time Maude verification model from a XML property file.
- The verification model can be also found in the AADL Navigator sidebar.

The screenshot displays the AADL Maude Property editor interface. The left sidebar shows the AADL Navigator with a tree view containing 'ActiveStandBy', 'aadl', 'aaxl', 'verification', 'rtmaude', and 'Plugin_Resources'. The main editor window shows the 'AADL Maude Property' for 'Main_ActiveStandBySystem_impl_Instance.maude'. The code includes several LTL specifications:

```
load Main_ActiveStandBySystem_impl_Instance.maude
load rtmaude/analysisFunctions.maude

(ctomod Main_ActiveStandBySystem_impl_Instance-VERIFICATION-DEF is
including MainActiveStandBySystemimplInstance .
including LTL-MODEL-CHECK-AADL .
including SIMPLE-COUNTEREXAMPLE .

op side1Active : -> Formula .
eq side1Active
  = value of side1ActiveSide in component (MAIN -> sideOne -> sideProces
op side2Active : -> Formula .
eq side2Active
  = value of side2ActiveSide in component (MAIN -> sideTwo -> sideProces
op agreeOnActiveSide : -> Formula .
eq agreeOnActiveSide
  = ((value of side1ActiveSide in component (MAIN -> sideOne -> sideProces
    ^
      (value of side2ActiveSide in component (MAIN -> sideTwo ->
        v
          (value of side1ActiveSide in component (MAIN -> sideOne
            ^
              (value of side2ActiveSide in component (MAIN -> sideTwo
op side1FullyAvailable : -> Formula .
eq side1FullyAvailable
  = value of side1FullyAvail in component (MAIN -> sideOne -> sideProces
op side2FullyAvailable : -> Formula .
eq side2FullyAvailable
  = value of side2FullyAvail in component (MAIN -> sideTwo -> sideProces
```

Problems Properties AADL Property Values Maude Console

Elapsed time: 00:00:00.054

Result Bool :
true

Maude>

Counterexamples (I)

- If a given LTL property is not satisfied in a model, then a counterexample is generated.
- We illustrate such counterexamples with an incorrect LTL specification for the active standby model.

The screenshot shows the AADL IDE interface. The main window displays the XML representation of an LTL property. The property is: $\square \square \neg (\text{noChangeAssumptionNextState} \rightarrow 0 \text{ (agreeOnActiveSide)})$. A red arrow points to the negation symbol (\neg) with the label "Negated formula". The IDE also shows a tree view on the left with the project structure, and a Maude console at the bottom showing the result of the verification: "Ready. Result Bool : true".

```
<?xml version="1.0" encoding="UTF-8" standalone="no"?>
<verification>
<model type="SynchAADL"/>ActiveStandBy/aaxl/packages/Main_ActiveStandBySys
<command>
<name>R1</name>
<value type = "ltl"><math>\square \square \neg (\text{noChangeAssumptionNextState} \rightarrow 0 \text{ (agreeOnActiveSide)})</math>
</command>
<command>
<name>R2a</name>
<value type = "ltl"><math>\square \square ((\text{noChangeAssumptionNextState} \wedge 0 \text{ side1FullyAvailable}) \rightarrow 0 \text{ (side1FullyAvailable)})</math>
</command>
<command>
<name>R3g</name>
<value type = "ltl"><math>\square \square (\neg \text{manSelectPressed} \wedge \text{agreeOnActiveSide} \wedge \text{side1FullyAvailable}) \rightarrow 0 \text{ (side1FullyAvailable)}</math>
</command>
<command>
<name>R4</name>
<value type = "ltl"><math>\square \square (((\text{side1Failed} \wedge \neg \text{side2Failed}) \rightarrow 0 \text{ (side2FullyAvailable)}) \wedge \text{side1FullyAvailable}) \rightarrow 0 \text{ (side2FullyAvailable)}</math>
</command>
<command>
<name>R5side1</name>
<value type = "ltl"><math>\square \square (((\text{side1Active} \wedge \text{side1FullyAvailable} \wedge \neg \text{manSelectPressed}) \wedge \text{side1FullyAvailable} \wedge \neg \text{side2FullyAvailable}) \wedge \neg \text{manSelectPressed} \wedge \neg \text{side1Failed}) \rightarrow 0 \text{ (side1FullyAvailable)}</math>
</command>
</verification>
```

Verification | Main_ActiveStandBySystem_impl_Instance.prop

Problems | Properties | AADL Property Values | Maude Console

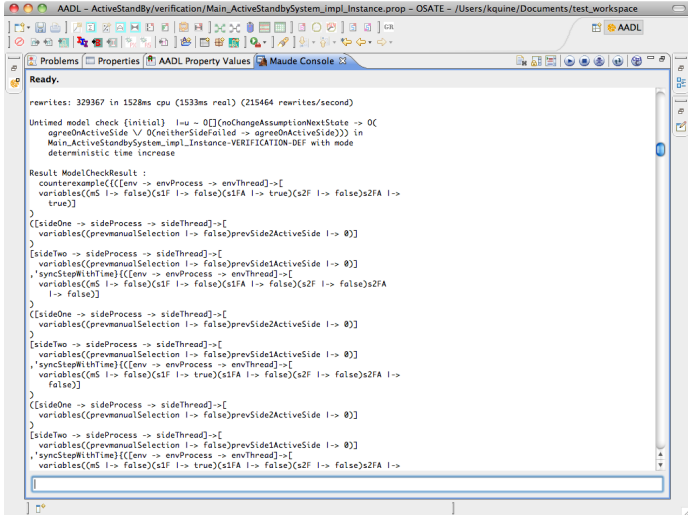
Ready.

Result Bool :
true

Maude>

Counterexamples (II)

- Here is a generated counterexample in SynchAADL2-Maude.
- For each state, a component name and its local variables are displayed.



```
AA DL - ActiveStandBy/verification/Main_ActiveStandBySystem_impl_Instance.prop - OSATE - /Users/kquine/Documents/test_workspace  
Problems Properties AADL Property Values Maude Console  
Ready.  
rewrites: 329367 in 1528ms cpu (1533ms real) (215464 rewrites/second)  
Untimed model check (initial) l-u ~ 0[] (noChangeAssumptionNextState -> 0C  
agreeOnActiveSide  $\vee$  0(neitherSideFailed -> agreeOnActiveSide))) in  
Main_ActiveStandBySystem_impl_Instance-VERIFICATION-DEF with mode  
deterministic time increase  
Result ModelCheckResult :  
  counterexample{[[[env -> envProcess -> envThread]->[  
    variables(mS l-> false)(s1F l-> false)(s1FA l-> true)(s2F l-> false)s2FA l->  
      true]]  
  ]  
  }  
  [[sideOne -> sideProcess -> sideThread]->[  
    variables((prevmanualSelection l-> false)prevSide2ActiveSide l-> 0)]  
  ]  
  }  
  [sideTwo -> sideProcess -> sideThread]->[  
    variables((prevmanualSelection l-> false)prevSide1ActiveSide l-> 0)  
    , 'syncStepWithTime{[[[env -> envProcess -> envThread]->[  
      variables(mS l-> false)(s1F l-> false)(s1FA l-> false)(s2F l-> false)s2FA  
        l-> false]]  
    ]  
    }  
  ]  
  }  
  [[sideOne -> sideProcess -> sideThread]->[  
    variables((prevmanualSelection l-> false)prevSide2ActiveSide l-> 0)]  
  ]  
  }  
  [sideTwo -> sideProcess -> sideThread]->[  
    variables((prevmanualSelection l-> false)prevSide1ActiveSide l-> 0)  
    , 'syncStepWithTime{[[[env -> envProcess -> envThread]->[  
      variables(mS l-> false)(s1F l-> true)(s1FA l-> false)(s2F l-> false)s2FA l->  
        false]]  
    ]  
    }  
  ]  
  }  
  [[sideOne -> sideProcess -> sideThread]->[  
    variables((prevmanualSelection l-> false)prevSide2ActiveSide l-> 0)]  
  ]  
  }  
  [sideTwo -> sideProcess -> sideThread]->[  
    variables((prevmanualSelection l-> false)prevSide1ActiveSide l-> 0)  
    , 'syncStepWithTime{[[[env -> envProcess -> envThread]->[  
      variables(mS l-> false)(s1F l-> true)(s1FA l-> false)(s2F l-> false)s2FA l->  
        false]]  
    ]  
    }  
  ]  
  }
```

Thank you!